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CONFIRMATION NO. 5698

SERIAL NUMBER 10/562,075	FILING OR 371(c) DATE 12/22/2005 RULE	CLASS 324	GROUP ART UNIT 2858	ATTORNEY DOCKET NO. DE03 0213 US1
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**APPLICANTS**

Martin Kadner, Hamburg, GERMANY;

**\*\* CONTINUING DATA \*\*\*\*\***

This application is a 371 of PCT/IB04/50897 06/14/2004

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**\*\* FOREIGN APPLICATIONS \*\*\*\*\***

EUROPEAN PATENT OFFICE (EPO) 03101869.0 06/25/2003

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**IF REQUIRED, FOREIGN FILING LICENSE GRANTED**

\*\* 05/05/2006

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY GERMANY	SHEETS DRAWING 1	TOTAL CLAIMS 2	INDEPENDENT CLAIMS 2
Verified and Acknowledged Examiner's Signature	Initials				

**ADDRESS**

24738

**TITLE**

Method and circuit arrangement for the self-testing of a reference voltage in electronic components

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